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L1	2	"6167092".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 08:57
L2	18224	"708"/\$.ccls. and @ad<"20010330"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 08:59
L3	848	2 and 708/400-409.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 08:59
L4	199	(IDCT or (inverse\$1 adj DCT) or (inverse\$1 adj discrete adj cosine)) and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 11:32
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L8	6	("5309527"   "5539836"   "5668748").PN. OR ("6587590").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/27 10:19

L9	18	("5053985"   "5181183"   "5249146"   "5345408"   "5452466"   "5574661"   "5610849"   "5636152"   "5671169"   "5724278"   "5825420"   "5986709"   "5995990"   "Re34734").PN. OR ("6295320").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/27 10:23
L10	9	("5216516"   "5576958"   "5739873").PN. OR ("5883823").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/06/27 10:35
L11	509	375/240.2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 11:32
L12	337	(IDCT or (inverse\$1 adj DCT) or (inverse\$1 adj discrete adj cosine)) and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 11:32
L13	201	12 and (macroblock\$1 or macro-block\$1 or (macro adj block\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 11:33
L14	191	13 and decod\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/27 11:34


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Pai, C.-Y.; Lynch, W.E.; Al-Khalili, A.J.;

Vision, Image and Signal Processing, IEE Proceedings- Volume 150, Issue 4, 22 Aug. 2003 Page(s):245 - 255

[AbstractPlus](#) | [Full Text: PDF\(691 KB\)](#) IEE JNL**3. Error-resilient image coding (ERIC) with smart-IDCT error concealment technique for multimedia transmission**

Yew-San Lee; Ong, K.-K.; Chen-Yi Lee;

Circuits and Systems for Video Technology, IEEE Transactions on Volume 13, Issue 2, Feb. 2003 Page(s):176 - 181

[AbstractPlus](#) | [References](#) | [Full Text: PDF\(1449 KB\)](#) IEEE JNL**4. A low-power and low-complexity DCT/IDCT VLSI architecture based on backward recursion**

An-Yeu Wu; Liu, K.J.R.;

Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on Volume 4, 30 May-2 June 1994 Page(s):155 - 158 vol.4

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Mokry, R.; Anastassiou, D.;

Circuits and Systems for Video Technology, IEEE Transactions on Volume 4, Issue 4, Aug. 1994 Page(s):392 - 406, 447-8, 499

[AbstractPlus](#) | [Full Text: PDF\(1172 KB\)](#) IEEE JNL**6. Minimum drift architectures for 3-layer scalable DTV decoding**

Vetro, A.; Huifang Sun; DaGraca, P.; Poon, T.;

Consumer Electronics, IEEE Transactions on

Volume 44, Issue 3, Aug. 1998 Page(s):527 - 536

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